FIG.1

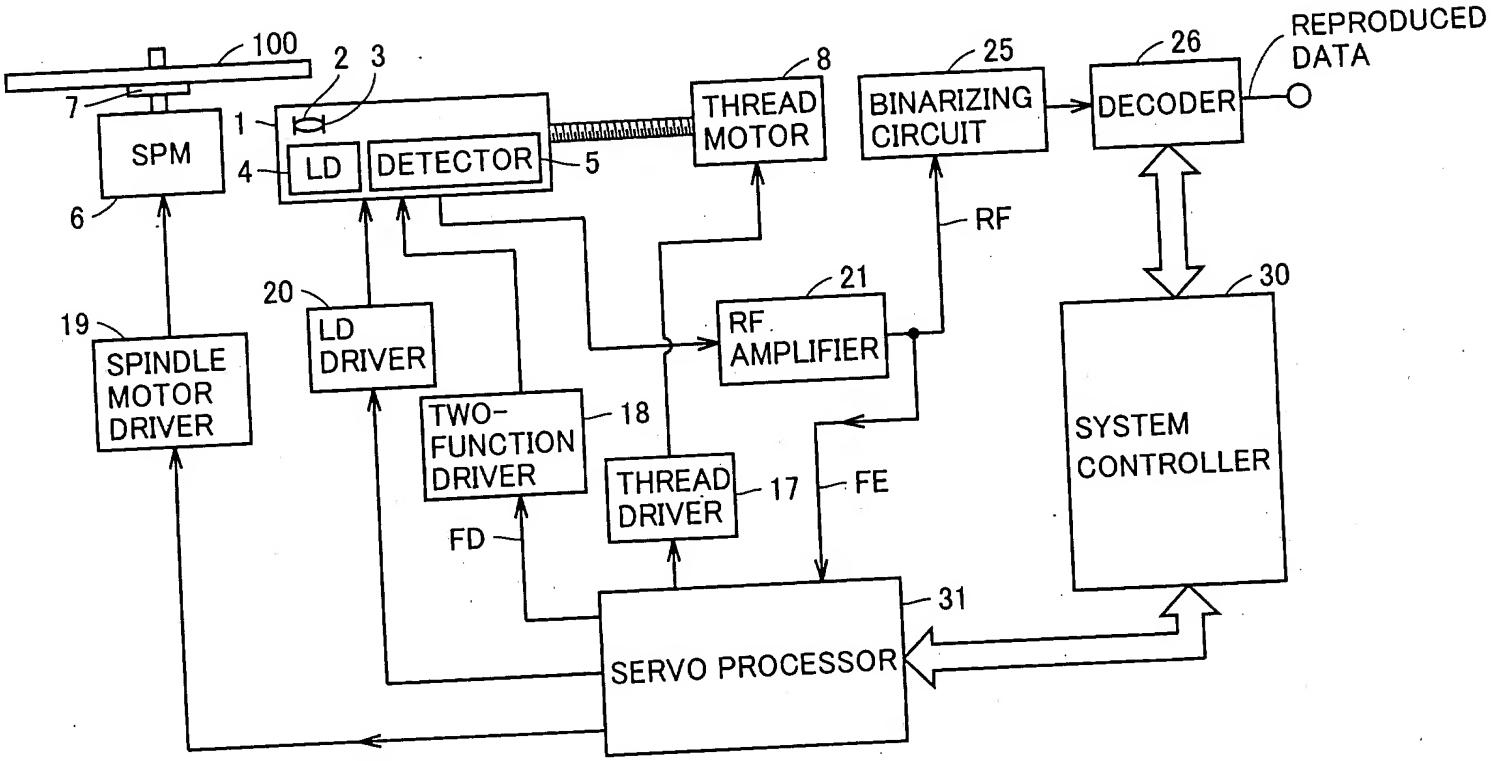
100
2
3
7
1
SPM
4
LD
DETECTOR
5
6
8
THREAD MOTOR
25
BINARIZING CIRCUIT
26
DECODER
REPRODUCED DATA
RF
20
LD DRIVER
21
RF AMPLIFIER
30
19
SPINDLE MOTOR DRIVER
TWO-FUNCTION DRIVER
18
THREAD DRIVER
17
FE
SYSTEM CONTROLLER
FD
31
SERVO PROCESSOR

FIG.2

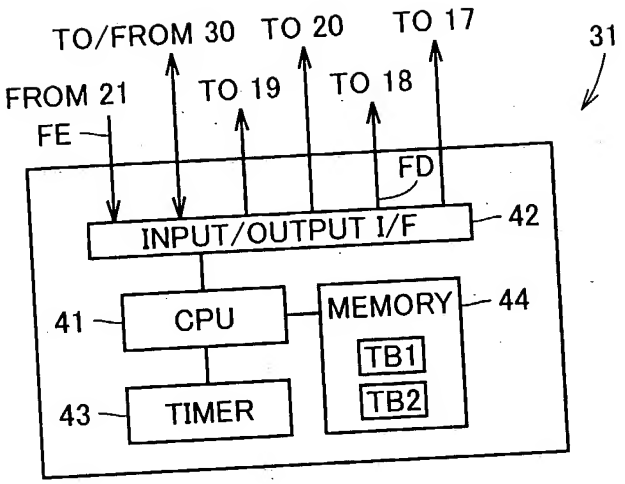

FIG.3A

TB1

FL1	LV:SPECIFICATION LEVEL
1	×××
0	△△△

FIG.3B

TB2

RLI

RECORDING LAYER INFORMATION	FL2
RECORDING LAYER 102	"1"("0")
RECORDING LAYER 104	"0"("1")
⋮	⋮

FIG.4

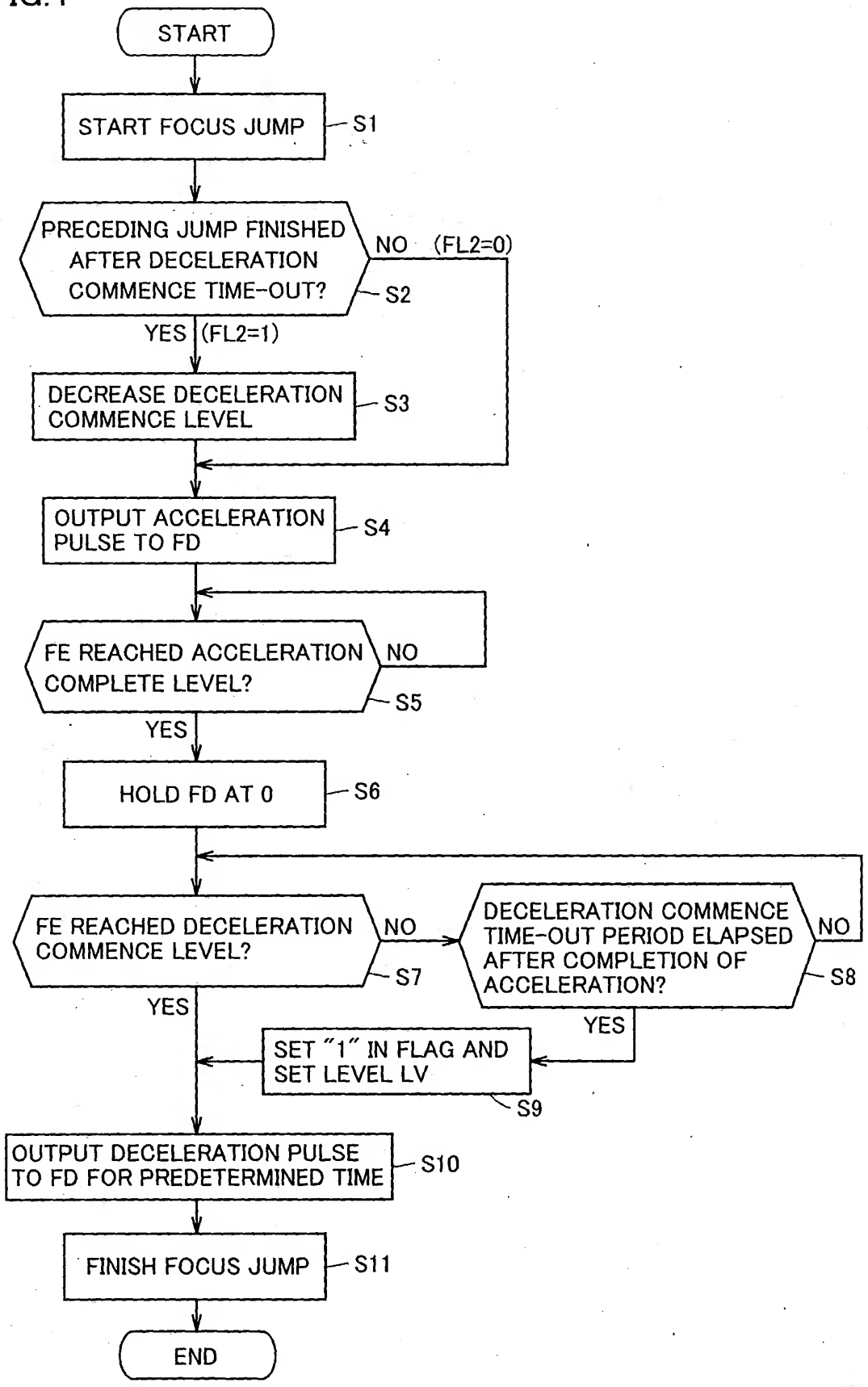
START
START FOCUS JUMP
S1
PRECEDING JUMP FINISHED AFTER DECELERATION COMMENCE TIME-OUT?
S2
NO (FL2=0)
YES (FL2=1)
DECREASE DECELERATION COMMENCE LEVEL
S3
OUTPUT ACCELERATION PULSE TO FD
S4
FE REACHED ACCELERATION COMPLETE LEVEL?
S5
NO
YES
HOLD FD AT 0
S6
FE REACHED DECELERATION COMMENCE LEVEL?
S7
NO
DECELERATION COMMENCE TIME-OUT PERIOD ELAPSED AFTER COMPLETION OF ACCELERATION?
S8
NO
YES
YES
SET "1" IN FLAG AND SET LEVEL LV
S9
OUTPUT DECELERATION PULSE TO FD FOR PREDETERMINED TIME
S10
FINISH FOCUS JUMP
S11
END

FIG.5

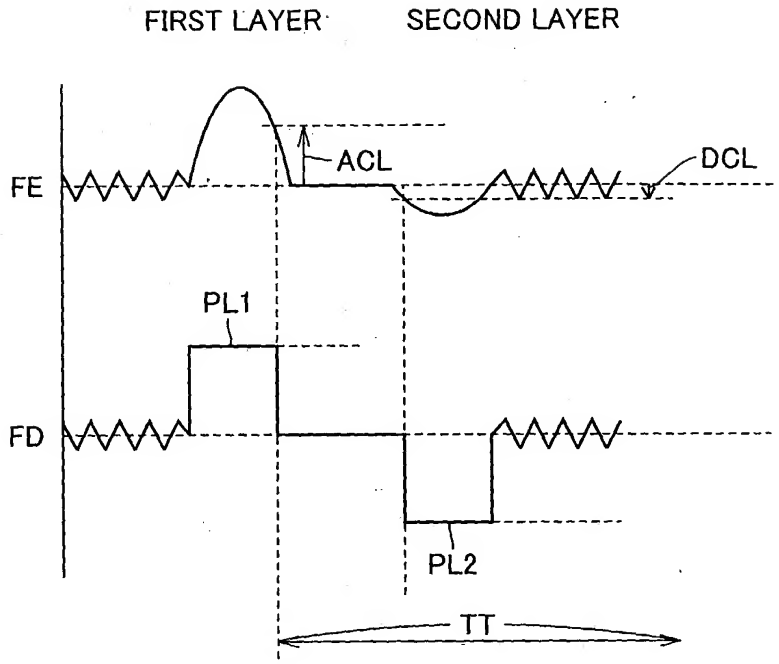

FIG.6 PRIOR ART

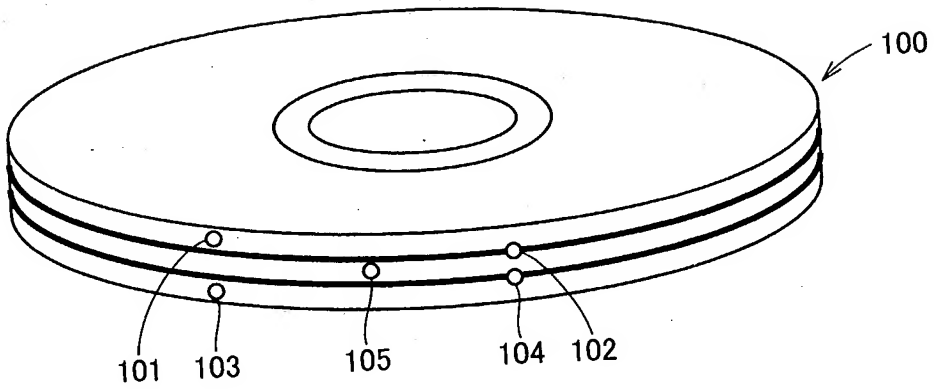

FIG.7 PRIOR ART

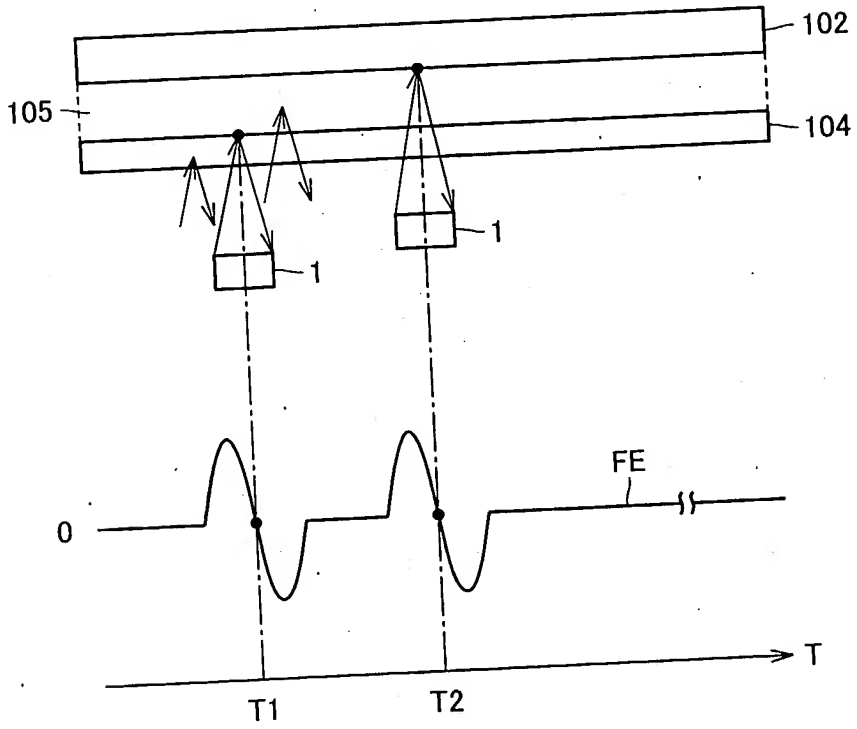

FIG.8 PRIOR ART

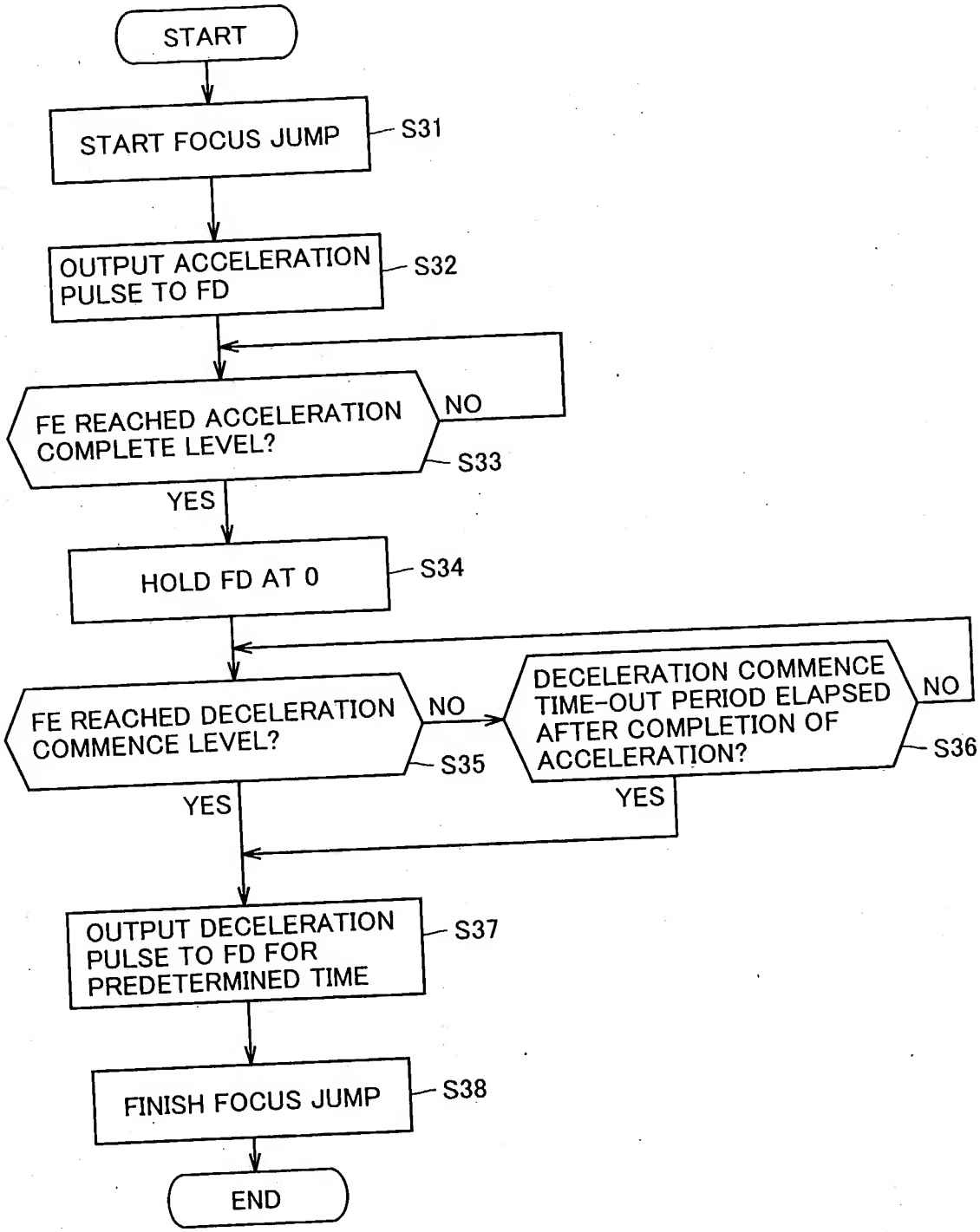

FIG.9 PRIOR ART

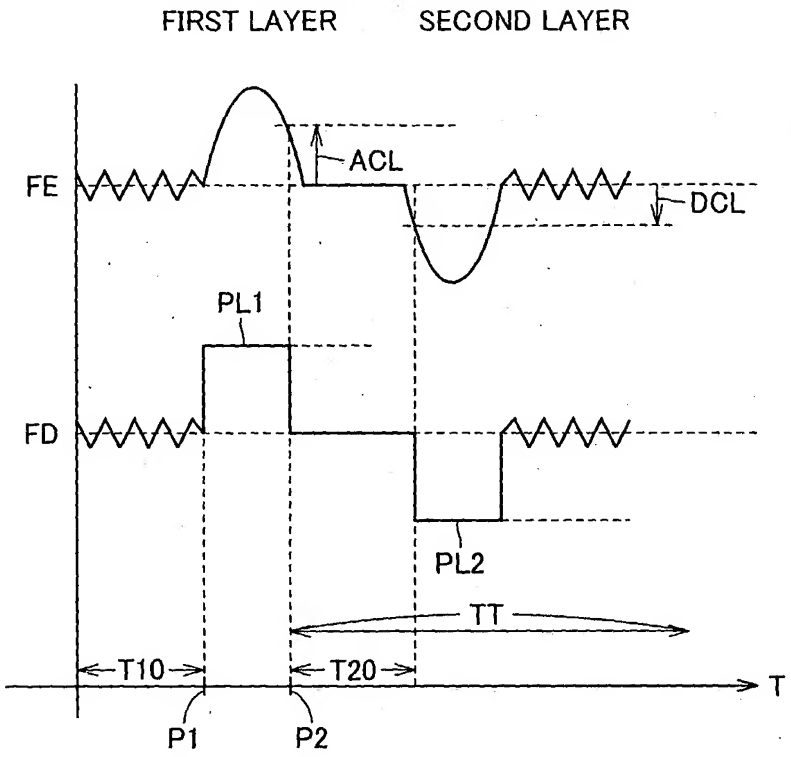
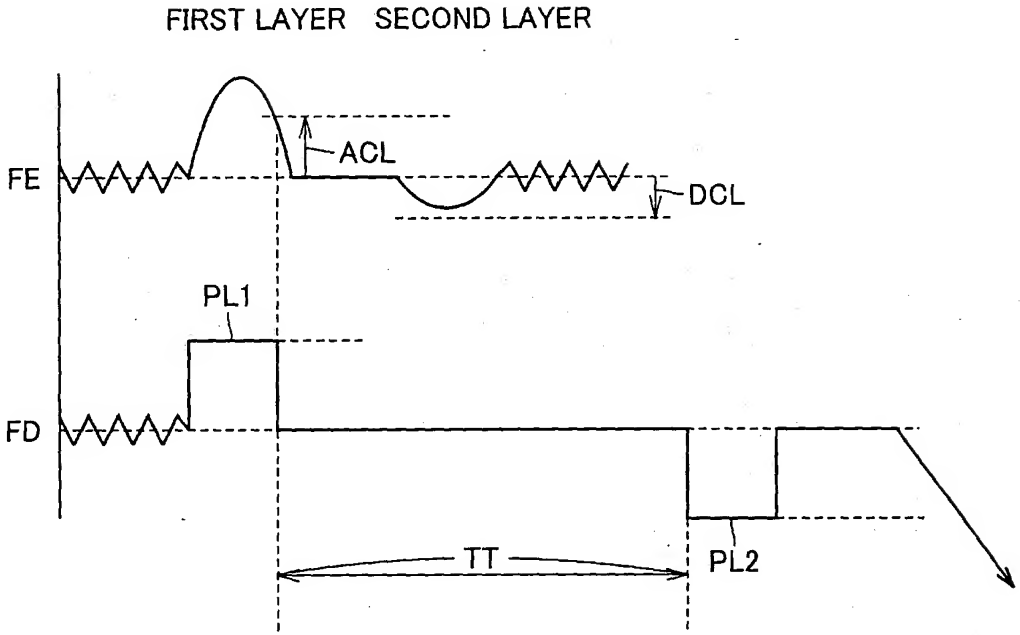
FIG.10 PRIOR ART